

Ser. No. 10/510,058
Amdt. dated March 7, 2006
Reply to Office action of September 7, 2005

PU020090

Remarks/Arguments

Specification

The Specification was objected to due to a following reason. The examiner stated the following subject matter claimed in Claim 1: "a power supply output transistor for generating a first power supply current of a load circuit and a second power supply current of said first amplifier stage" is not disclosed in the Specification.

To increase clarity of the claims, applicant has amended the claims to remove the reference to "a power supply output transistor". Instead applicant recites the power supply described in the specification and illustrated in drawing Figure 2b. As a result, applicant believes the claims are fully supported by the specification such that one of ordinary skill in the art could make and use the invention. Therefore, applicant respectfully requests withdrawal of the objection to the specification.

35 U.S.C. 103(a)

Claims 1 and 2 were rejected under 35 U.S.C. 103(a) as being unpatentable over Chambers et al. (US 3,818,128) in view of Watanabe et al. (US 6,373,671) and Applicant Admitted Prior Art (AAPA). The office action states Chambers et al. disclose following elements of the claim including: a main deflection circuit for generating a main deflection current in a main deflection winding to scan an electron beam on a screen of a cathode ray tube (31 in Fig. 2); a first amplifier stage for generating an auxiliary deflection current in an auxiliary deflection winding (23 in Fig. 2) to correct a raster distortion (27 in fig. 2); a power supply output transistor generating a first power supply current of a load circuit and a second power supply current of said first amplifier stage (239 and 240 in Fig. 4).

However, the office action recognizes Chambers does not disclose a sensor and a switch for controlling the power supply of the first amplifier stage. The office action states Watanabe et al. disclose a single power source (2 in Fig. 1) feeding plural loads (10a -10d in Fig. 1) and including a sensor (3a in Fig. 1) detecting an occurrence a fault condition in a current path of said power supply current of the first load (10a in Fig. 1) and a first power switch (9a in Fig. 1) responsive to an

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output of the sensor selectively reducing the second power supply current of the first load, without interrupting the power supply current of other load circuits, when the fault condition occurs (col. 5, line 54 - col. 6, line 65).

Therefore, the office action states it would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified the Chambers et al. solution by adding the current protection solution of Watanabe et al., because according to AAPA (page 1, lines 20 -26), because trouble shooting convenience requires that the TV receiver show a picture when the convergence circuit have a fault.

Applicant's amended claim 1 recites features not disclosed or suggested in either Chambers or Watanabe alone or in combination. Among other features not disclosed applicants claims recite, inter alia, ***"a power supply for developing a supply current of said main deflection circuit and a positive and a negative supply voltage of said first amplifier stage, said power supply developing said positive supply voltage before developing said negative supply voltage..."***

Applicant submits none of the prior art references cited in the office action discloses or suggests

"a power supply for developing a supply current of said main deflection circuit and a positive and a negative supply voltage of said first amplifier stage, said power supply developing said positive supply voltage before developing said negative supply voltage..."

Therefore, applicant believes the claims, as amended herein, are in condition for allowance and respectfully requests withdrawal of the rejection.

In the Drawings

The Examiner has objected to the drawings as failing to illustrate every feature of the invention specified in the claims. In particular, the examiner notes "a power supply output transistor for generating a first power supply current of a load circuit and a second power supply current of said first amplifier stage" must be shown or the features canceled from the claims.

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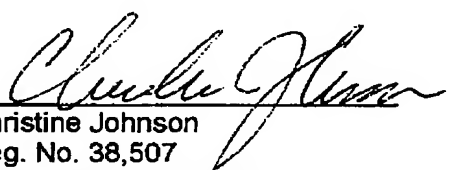
PU020090

Applicant has amended the claims to remove the recitation of " a power supply output transistor" . Instead applicant refers to a power supply as described in applicant's specification and illustrated in applicant's drawing figure 2b. No new matter has been entered.

In view of this amendment applicant believes the office objection to the drawings is moot and no correction is required.

Having fully addressed the Examiner's rejections it is believed that, in view of the preceding amendments and remarks, this application stands in condition for allowance. Accordingly then, reconsideration and allowance are respectfully solicited. If, however, the Examiner is of the opinion that such action cannot be taken, the Examiner is invited to contact the applicant's attorney at (609) 734-6892, so that a mutually convenient date and time for a telephonic interview may be scheduled.

Respectfully submitted,


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